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(21) International Application Number: PCT/US84/01980 (22) International Filing Date: 4 December 1984 (04.12.84) (71) Applicant (for all designated States except US): J. C. SCHUMACHER COMPANY [US/US]; 580 Airport Road, Oceanside, CA 92054 (US). (72) Inventor; and (75) Inventor/Applicant (for US only) : SCHUMACHER, John, C. [US/US]; 580 Airport Road, Oceanside, CA 92054 (US). (74) Agents: HUBBARD, Grant, L. et al.; Hubbard and Ste-tina, 24221 Calle de la Louisa, Suite 401, Laguna Hills, CA 92653 (US). (81) Designated States: AT (European patent), AU, BE (Eu-ropean patent), CH (European patent), DE (European patent), DK, FI, FR (European patent), GB (Euro-pean patent), JP, LU (European patent), MC, NL (Eu-ropean patent), NO, SE (European patent), US.		Published <i>With international search report.</i>
(54) Title: CONTINUOUSLY PULLED SINGLE CRYSTAL SILICON INGOTS (57) Abstract A method for producing single crystal ingots continuously by forming a molten body of silicon metal of two feed-stocks of silicon, one feedstock containing a predetermined level of dopant; continuously drawing a single crystal ingot of doped silicon from said molten body of silicon, said ingot being characterized in that the concentration of dopant is un-form along the length of the ingot, while continuously feeding said feedstocks into said molten body of silicon to thereby maintain the concentration of dopant uniform in said body during the drawing of the single crystal therefrom.		

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CONTINUOUSLY PULLED SINGLE CRYSTAL SILICON INGOTSField of the Invention

This invention relates to semiconductor grade silicon and, more particularly, to a method of producing large single crystal doped silicon ingots on a continuous basis.

Background of the Invention

Semiconductor grade silicon is a foundational to the large and growing semiconductor, computer, instrumentation and electronic industries. Semiconductor grade silicon is characterized by the requirement of ultrahigh purity, a level of purity not required and largely unattainable in other fields of chemical and metallurgical technology. Another characteristic of semiconductor grade silicon is the requirement, in some applications, that the silicon contain minute but precisely known or controlled amounts of specific impurities. This is known as "doping" silicon and the product is referred to as "doped" silicon. Exemplary of the elements with which silicon is doped are boron, phosphorous, arsenic and antimony. Boron is a typical "electron acceptor" and phosphorous is a typical "electron donor". Silicon doped in specified areas or throughout a body of silicon with either boron or phosphorous, for example, or with both boron and phosphorous in different regions of the silicon body, becomes an electron valve, commonly referred to as a semiconductor and can perform a great variety of switching, amplification, memory and other electronic functions in suitable electron control circuits. The semiconductor industry is well developed now and there are thousands of semiconductor devices marketed directly or assembled into computers, radios, televisions, controllers and nearly an infinite variety of other electronic devices.

Foundational to the semiconductor industry is a smaller but still well developed semiconductor materials supply industry which supplies silicon and silicon compounds, compounds for doping silicon (called "dopants"), and doped silicon, as well as other chemicals and a variety of silicon based components in finished or partially manufactured form.

One form of silicon widely used in the semiconductor industry is the semiconductor grade single crystal silicon ingot. Single crystal silicon ingots, of semiconductor grade, are produced using a very well-known and widely used classical technique for growing single crystals known as the Czochralski method, sometimes referred to as the CZ method. Single crystal ingots of metals are grown according to the Czochralski method by contacting a small single crystal of the metal to be grown with a molten body of the metal and drawing the single crystal away from the molten body of metal slowly while rotating the single crystal. The single crystal is kept at a temperature lower than the melting point of the crystal. The layer of molten metal adjacent the crystal, in immediate contact with the crystal, only a few atoms thick, deposits on the single crystal seed and the seed grows. The atoms of the molten metal deposit in the same crystal structure as the seed and, thus, a larger single crystal is formed. This process continues with layer upon layer of single crystal being deposited upon the growing ingot until a large ingot is formed. These ingots may be very large, weighing upward of 100 kg typically and may be several inches in diameter and a few feet in length. If the seed and the molten metal are of high purity metal, such as semiconductor silicon in the present context, then the result is a single crystal of ultrahigh purity semiconductor grade silicon

in which the crystal structure is virtually perfect. A number of adaptations and variations for growing single crystals are known, see, for example, U.S. Patents Nos. 3,998,598, 4,282,184, 4,410,494, 4,454,096 and 4,458,152.

The Czochralski method is not without limitations, however, and while the production of perfect ultrahigh purity silicon is one of technology's crowning achievements, the method is subject to serious problems. The Czochralski method is generally performed as a batch process. A given quantity of silicon from any convenient source is melted in a crucible and drawn on a seed crystal until the molten metal is depleted. If the silicon feedstock were perfectly pure, and if there were no impurities introduced in the process, then a perfect single crystal of perfectly uniform purity could, in theory, be produced. Such is not possible, however, and the presence of even minute impurities creates non-uniformity in the ingot purity. As the ingot is drawn from the melt, a phenomenon known as "partitioning" occurs wherein the impurities preferentially migrate into the crystal being grown or remain preferentially in the melt -- the latter being more common. Typically, the partitioning effect favors build up of impurities in the melt. Thus, as the crystal is grown, the concentration of impurities in the melt increases. Since the partition effect results in a relatively constant percentage of the impurity present in the melt being partitioned into the crystal, as the level of impurity in the melt increases during growth of the crystal, the level of the impurity in the crystal also increases. Thus, the impurity level in the crystal increases as the crystal is grown. (The converse would occur if the impurities partitioned preferentially into

the crystal.) In a device in which parts per billion of the impurity can drastically change electronic characteristics this impurity gradient is a serious problem indeed. This problem is most often over come by simply limiting the size of the crystal such that the impurity gradient along the crystal does not significantly effect the electronic characteristics.

It is desirable to produce a single crystal ingot of silicon which has throughout the crystal a known or controlled amount of dopant, such that the electronic characteristics differ from those of ultrapure silicon but are uniform at all points along the length of the ingot. It will be apparent from the foregoing that the Czochralski batch process is not well suited to the production of a uniformly doped single crystal ingot of silicon. A principle feature of the present invention is a method of growing single crystal silicon ingots by a modified Czochralski process on a continuous or semicontinuous basis wherein the ingots have a uniform level of dopant along the length of the ingot.

Various methods are known for the production of semiconductor grade silicon. An early method for producing high purity silicon involved the thermal decomposition of chlorosilane or silicon tetrachloride on a high temperature filament, such as a tungsten or tantalum filament. Another such method is described in Austrian Patent No. 207,362 issued January 25, 1960, entitled "Method of Winning Silicon," in which silicon is formed by the thermal decomposition of chlorosilanes and silicon tetrachloride. The silicon adheres strongly to the quartz tube and is recovered by the breaking to the quartz tube during or after cooling. A like process is described in U.K. Patent No. 924,545, April 24, 1963, "Process for the manufacture of Silicon of High Purity." A variation of the above process is

described in U.S. Patent No. 3,963,838 to Setty et al, "Method of operating a Quartz Fluidized Bed Reactor for the production of Silicon, wherein the silicon deposit on the quartz walls does not exceed 2 mills and shatters and peels from the walls during production. The production of silicon on nucleating silicon particles is taught in U.S. Patent No. 3,012,862, December 12, 1961, to Bertrand et al, and U.S. Patent No. 3,012,861, December 12, 1961, to Ling. Production of seed particles is described in U.S. Patent No. 4,207,360, June 10, 1980, to Padovani. Variations in the thermal decomposition method of silicon production are described in U.S. Patent No. 4,117,094, September 26, 1978, to Blocher, Jr., et al, and in U.S. Patent No. 4,265,859, May 5, 1981, to Jewett.

Garagaglia, et al, U.S. Patent No. 4,309,241, January 5, 1982, describe the production of silicon by drawing a slim rod of silicon on a seed crystal from a silicon melt through a chemical vapor deposition chamber to produce an enlarged single crystal semiconductor body. This is a variation of the classical Czochralski method of growing single crystals on a seed crystal; however, in the Garagaglia et al process the bulk of the silicon production occurs by vapor decomposition on the surface of the silicon ribbon drawn from the melt, rather from the molten body of silicon.

The production of ultrahigh purity semiconductor grade silicon from tribromosilane is described in U.S. Patents Nos. 4,084,024, Joseph C. Schumacher, April 11, 1978, and 4,318,942, Woerner, et al, March 9, 1982.

Thus, while it is known in the prior art to produce large ingots of silicon semiconductor material, this is done on a batch basis either by the Czochralski method of growing a single crystal from a

melt or by vapor deposition on a heated filament or ribbon of silicon, these processes are limited by the boundary conditions attaching to the batch process and by the partitioning of impurities. The present invention overcomes these limitations and inadequacies of the prior and permits the production of ultrahigh purity semiconductor grade silicon ingots and of ingots of doped silicon having a constant level of doping on a continuous or semicontinuous basis.

Efforts have been made to produce single crystal silicon by the Czochralski method on a continuous basis but, insofar as is known, no such methods have been found to be fully reliable and completely satisfactory. One of the principle problems with a continuous crystal drawing method of the type described is that the growth of a high quality, high purity single crystal is very dependent upon maintaining a stable mass and thermal balance in the melt. Even a minor thermal disturbance can upset the crystal growth and result in a polycrystalline ingot which would normally have to be re-melted as scrap. This, obviously, a very expensive and undesirable occurrence. The silicon feed stocks of the prior art are typically made up of large or small agglomerations or chunks of silicon, often of greatly variable size, shape and surface area. Thus, the material is difficult to handle and cannot be fed into a crucible in a precisely metered manner. The act of providing, or attempting to provide, a continuous feed tends to disturb the CZ furnace heat balance and stability thus causing or increasing the risk of a defective crystal growth. If the silicon is crushed into fine powder, it acquires an enormous surface area and acquires substantial impurities simply from the crushing operation. With the large, irregular surface area of

such a product, it is virtually impossible to feed silicon in crushed or comminuted form into a furnace without introducing large amounts of oxygen and other adsorbed and absorbed impurities into the melt.

5 Product quality always suffers and the risk of large numbers of very expensive rejected ingots is significantly increased. When one considers that a single ingot may be sawed into several thousand wafers each of which may form from several dozen to several
10 hundred semiconductor devices, or integrated circuits having thousands of semiconductor devices formed therein, the importance of a reliable method of producing perfect single crystal ingots of silicon can be appreciated. An important feature of the present
15 invention is that it solves all or most of the problems of the prior art respecting continuous growth of silicon ingots and thus opens the way for the economic production of extremely high quality, ultrahigh purity, perfect single crystal silicon ingots on a
20 substantially continuous basis.

Summary of the Invention

The present invention may be described as a method for continuously producing semiconductor grade silicon by (a) producing spherical, low surface to volume
25 ratio, nonagglomerated individual monodisperse particles of silicon having a diameter of from about 1/2 to about 2 mm, (b) forming a molten body of silicon, (c) continuously feeding the monodisperse particles of silicon of step (a) into the molten body
30 of step (b), and (d) drawing a single crystal of silicon continuously from the molten body of silicon of step (b) to thereby form an ingot of ultrahigh purity silicon having substantial uniform composition along the entire length of the ingot.

35 The present invention is most advantageous as a

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method for continuously producing an ingot of silicon having a constant level of doping along the length thereof by (a) forming a body of molten silicon, (b) continuously feeding into said molten body ultrahigh purity spherical, low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon having a diameter of from about 1/2 to about 2 mm, (c) producing spherical, low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon containing a predetermined quantity of dopant and having a diameter of from about 1/2 to about 2 mm, (d) continuously feeding into said molten body the particles of step (c), and (e) continuously drawing a single crystal of silicon having a constant level of dopant from the molten body of dopant containing silicon to thereby produce an ingot of semiconductor grade silicon having a uniform concentration of dopant therein along the length of the ingot.

The present invention may also be described as a method of producing on a continuous basis doped silicon ingots having constant composition along the length thereof, comprising feeding two streams of spherical, low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon into a molten body of silicon, one of said streams being of higher purity than the other stream, the other stream comprising silicon containing dopant, and continuously pulling a single crystal from the molten body.

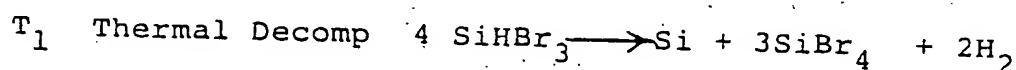
The present invention encompasses the very important and unexpected discovery of a unique form of silicon, and singularly striking and unobvious use of such silicon in a new method for producing perfect single crystal silicon ingots on a continuous basis.

Description of the Invention

The Schumacher Silicon Process (SSP), which is described and claimed herein, involves the recognition that a unique form of silicon makes possible a new process with a new result not hitherto known. Semiconductor grade

silicon can be produced in the form of spherical, low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon having a diameter of about 1 mm average with a maximum size distribution from about 1/2 to about 2 mm in diameter. This product is a free flowing "shot-like" product with each sphere being very nearly perfect, and approximately the same size as every other sphere. Semiconductor grade as used here is as defined in the semiconductor device fabrication industry, e.g., 0.1 ppb boron, 0.3 ppb phosphorus, etc. This feedstock is formed by thermal decomposition or hydrogen reduction of bromosilanes at 600°C to 1000°C at about atmospheric pressure in a "fluidized bed" reactor to achieve a product diameter from 1/2 millimeter to 2 millimeters. Suitable substrate particles for feedstock to the fluid bed reactor can be created by crushing or attrition of larger particles. The form of the small substrate particles is unimportant - only their purity and freedom from contamination. The spherodicity of the product of Step 1 of the Schumacher Silicon Process is developed during deposition onto these "substrate" particles fed into the Step 1 fluid bed reactor. The deposition results of course from the thermal decomposition or hydrogen reduction of the bromosilane compound to produce silicon and various by-products depending on the exact bromosilane and decomposition reduction method chosen.

It is critical that sintering be avoided in this step. Sintering avoidance is accomplished in the bromosilane system by its low decomposition temperature, being lowest for thermal decomposition, and raised by hydrogen dilution as a result of some hydrogen reduction, e.g.



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T_2 being higher than T_1 .

Sintering is a result of surface diffusion under the driving force of surface curvature which causes a distribution of particles to eliminate small particles and grow large particles, reducing the overall, net surface to volume ratio, and binds together the particles in contact with one another to again reduce the system surface/volume ratio.

The Schumacher Silicon Process (SSP) Step 1 product is monodisperse, it having a size from about 1/2 mm to 2 mm, hence there are no small particles tending to evaporate and deposit on, or grow larger particles that exist in the system. Furthermore, sintering takes place to an appreciable, or noticeable extent at about 60 percent of T_{mp} °K where T_{mp} is the melting point of the sintering species. For silicon $T_{mp} = 1420^\circ\text{C}$ and $1693^\circ\text{K} \times 0.6 = 1016^\circ\text{K} - 273 = 743^\circ\text{C} = T_{\text{sintering}}$. Thus, at temperatures above about 750°C , sintering can start to take place. As a practical matter, however, since the Step 1 reactor is a fluid bed reactor, contact times between particles are of a short duration, so that sintering does not become a problem until somewhat higher temperatures, at about $1000^\circ\text{C} - 1050^\circ\text{C}$ due to this and the monodisperse character of the product.

In Step 2, doped silicon is produced for addition of donors and acceptors to the continuous CZ melt. Step 2 operates identically to Step 1, except that the bromosilane working fluid employed in the process is not as pure as possible, but is taken from that part of the process where donors or acceptors are concentrated. Make-up working fluid and BBr_3 or PBr_3 are added to the feed streams to cause sufficient boron or phosphorus doped polycrystalline silicon of extremely low metals content (semiconductor grade) to be produced in a fluid bed reactor designated for this purpose.

Step 3 of the SSP is the continuous or semicontinuous

production of constant composition single crystal silicon
via the CZ process of seed withdrawal from the melt.
With current technology which employs batch melting of a
specific quantity (5-10 up to 60 or 100 or more kg) of
semiconductor grade polycrystalline silicon produced via
the Siemen's Process, or a fluid bed process which does
not utilize bromosilane chemistry and is, therefore,
incapable of producing continuous CZ feedstock from Steps
1 and 2, a partitioning of impurities occurs across the
solid-liquid phase boundary, since impurities, including
donors and acceptors, exist in different equilibrium
concentrations in a liquid in equilibrium with its solid
phase.

Thus, as the finite quantity "batch" of molten
silicon is solidified in the CZ process, concentration of
impurities vary with time in the melt and, therefore, with
position in the solid solidified from that melt.

In the SSP, this partitioning is avoided for two
reasons: (1) It is not a batch process, so that "boundary
values" do not interact with operation of the process;
and, (2) The melt composition is maintained constant and,
therefore, the solid composition is also constant,
irrespective of position in the solid (at least in the
direction of the "pull" axis).

Constant melt volume is accomplished via addition of
feedstock of SSP, Step 1 product, to exactly offset the
quantity per unit time removed from the melt via
solidification.

An important feature of the present invention is the
step of adding a controlled number of spherical, low
surface to volume ratio, nonagglomerated individual
monodisperse particles of silicon having a mean diameter
of about 1 mm to the silicon melt at a constant rate,
typically sphere-by-sphere to substantially exactly
replenish the melt as the single crystal grows, but
without disrupting or disturbing the mass or thermal
balance and stability of the crucible or melt. Since the

solid feedstock is introduced in particles characterized as spherical, thus having the minimum possible surface to volume ratio, and being nonagglomerated individual monodisperse particles of silicon having an approximately uniform diameter of about 1 mm, the effect on the mass of the melt, and upon the heat balance, i.e., the heat required to melt the added silicon and compensate for losses to the single crystal and to the environment, is a function of the rate of introduction of the individual particles. Since the rate of introduction of individual particles, one or more at a time, of substantially identical heat capacity can be controlled and kept constant, there are no disturbances or "spikes" in the heat absorbed in the system or required of the heating source. It is difficult to overestimate the importance of this facet of the invention, as it makes possible the very reliable growth of perfect single crystal silicon on a continuous basis.

Melt composition is maintained essentially constant with respect to the desired constituent of the product ingot doping with additions of SSP using Step 2 product as feedstock.

Thus, the Schumacher Silicon Process comprises the following steps to produce a superior quality wafer for semiconductor device manufacturing, including integrated circuits and silicon photovoltaic solar cells.

1. Prepare via the fluid bed thermal decomposition of bromosilane, particularly tribromosilane (SiHBr_3) at moderate temperature ($600^\circ\text{C} - 1050^\circ\text{C}$) a dense, monodisperse, spherical, large diameter (.5-2 millimeter) semiconductor grade polycrystalline silicon shot.
2. Prepare similar sized "doped" polycrystalline silicon shot in the same manner except for addition of BBr_3 or PBr_3 to the fluid bed reactor.

3. Add to a CZ melt the product from Steps 1 and 2 to exactly equal silicon and dopant removed from the melt by solidification of single crystal silicon. Here, it is noted that the large size (approximately 1 mm in diameter) of the shot, along with its density, provides a sufficiently low surface to volume ratio to preclude excess oxygen contamination of the melt known to be a problem with older, smaller diameter, and less dense fluid bed products which have attempted to have been used as feedstock for continuous or semi-continuous CZ of semiconductor grade silicon. In addition, the monodisperse nature of the product from Steps 1 and 2 allow the thermal load on the system to be precisely controlled as crystal is withdrawn and shot added to the melt.

An advantage of the process is that it provides an improved quality wafer, as the ingot from which that wafer is sawed must be as uniform as possible. This uniformity is developed by the continuous, or semi-continuous, pulling of CZ crystal from a melt in which constant thermal gradients and constant constitutional gradients exist, and only build up of minor constituents takes place. About 300 ft. of crystal can be pulled prior to too much build up of minor constituents.

As a practical matter, additions to the melt must, in fact, melt prior to coming into contact with the growing solid-liquid interface, so that additions are made behind a weir or other arrangements to provide sufficient time for such additions to melt, prior to being brought by convection to the region of the growing solid-liquid interface.

The weir arrangement is the only feature distinguishing the SSP crucible from ordinary CZ.

crucibles. Thus, no new equipment, as compared with the CZ crystal drawing equipment, is required. Thus, equipment generally as described in the patents which describe crystal growing techniques and equipment referred to hereinbefore may be used with only minor modification. Crystal may be pulled from a shallow melt, or a deep melt. Various forms of heating, inductive, resistance, R F, microwave, etc. may be used. Electromagnetic fields to control wall contact and melt convection paths may be employed. All of these are known in the art of CZ pulling of single crystal silicon of semiconductor grade.

The apparatus in which SSP is operated differs from the standard crystal pull furnace only in that arrangement made for handling of the ingot as it is withdrawn from the melt. The standard tower will handle only a limited length ingot. In SSP, an arrangement is made to keep the crystal aligned to within the critical angle with the melt by side supports which also serve to support the load, at least partially.

The product ingot is then much longer than typical ingots, and has a much reduced variation with position of both minor and major solute species than conventional CZ ingots. This ingot is then sliced, lapped, and polished into wafers by conventional means. These wafers show little variation in doping, defects, or impurity concentration from one wafer to the next, and from one end of the ingot to the other, clearly a vast improvement in the state of the art.

Example 1

Ultrahigh purity monodisperse spherical particles of silicon were manufactured according to the process described in U.S. Patent No. 4,084,942. The particles are of high density, of nearly uniform size, from 1/2 to 2 mm and principally about 1 mm in diameter, and formed a free flowing product which was free of fines and dust.

Using the same process, described in U.S. Patent No. 4,084,942, but using as a feedstock tribromosilane



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containing a controlled concentration of dopant, e.g., boron tribromide or phosphorous tribromide, a product of the same physical appearance, size and characteristics, but with a known, uniform concentration of dopant is produced. The dopant concentration may be at any desired and effective level, generally in the range from 0.001 to 1 ppm, as this product will be used as feedstock to the silicon melt to contribute a low level of dopant. Higher dopant concentrations may be used as well, since any ratio of feedstocks can be used to control the dopant concentration in the silicon melt.

Finally, a body of molten silicon is made up of the desired ratio of feedstocks from the preceding steps, namely ultrahigh purity silicon and doped silicon, to form a melt of uniform composition. The composition is maintained uniform during the entire operation of the process by continuously feeding in the ratio of feedstocks required. A melt having 5 ppb dopant is maintained by feeding equal quantities of ultrahigh purity silicon, less than 0.1 ppb, and doped silicon having 10 ppb dopant. A single crystal of silicon, which may be of ultrahigh purity, is contacted into the melt and withdrawn while being rotated, according to the classical Czochralski technique, thus growing a single crystal ingot having a uniform composition along the entire length of 5 ppb. The single crystal may be drawn continuously from the melt for very long periods of time. Calculations indicated that a single crystal up to 300 feet long is entirely feasible, although equipment design and handling convenience suggest that a crystal of this long may not be efficiently handled.

In summary, the method of this invention comprises producing single crystal ingots continuously by forming a molten body of silicon metal of two feedstocks of silicon, one feedstock containing a predetermined level of dopant; continuously drawing a single crystal ingot of doped silicon from said molten body of silicon, said ingot being

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characterized in that the concentration of dopant is uniform along the length of the ingot, while continuously feeding said feedstocks into said molten body of silicon to thereby maintain the concentration of dopant uniform in said body during the drawing of the single crystal therefrom. The term continuous, or continuously, as used herein means carrying out the process while repeating the steps set forth either periodically, or without interruption. Thus, a continuous process, sometimes referred to as semi-continuous, would involve the repeated periodic introduction of silicon material feedstock while the crystal was being drawn from the melt, as well as constantly adding silicon material feedstock during drawing of every feedstock. Every feedstock inherently contains some impurity or additive, hence, the terms ultrapure silicon and doped silicon are used in the normal technical meaning of these terms. The two feedstocks could, within the scope of the invention, contain, respectively, two concentrations of the same dopant or concentrations, the same or different, of two or more dopants.

The method of the invention, then, may be described as including the steps of feeding a first feedstock into a molten body of silicon metal, said first feedstock comprising ultrahigh purity semiconductor grade silicon; feeding a second feedstock comprising ultrahigh purity semiconductor grade silicon to which a known amount of semiconductor dopant has been added; and, while carrying out the above-stated steps, drawing a single crystal of doped silicon from said molten body. The method of the invention preferably includes the steps of introducing into said molten body a first silicon composition characterized in being spherical, low surface to volume ratio, nonagglomerated individual, monodisperse silicon particles having a diameter of about 1 mm, and introducing into said molten body a second silicon composition characterized in being spherical, low surface to volume

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ratio, nonagglomerated individual, monodisperse doped silicon particles having a diameter of about 1 mm, and carrying out these steps while drawing from the molten body an ingot of doped silicon of semiconductor grade.

5 The doped silicon particles preferably contain boron, antimony, arsenic, or phosphorous. In a particularly beneficial form of the invention, the silicon particles and the doped silicon particles have a mean diameter of about 1 mm, and the particle feedstocks are substantially

10 free of sintered particles, particles substantially over 2 mm in diameter, and fine particles substantially under 1/2 mm in diameter.

Industrial Application

15 This invention finds wide and general application in the semiconductor industry.



WHAT IS CLAIMED IS:

1. The process of producing single crystal ingots continuously comprising the steps of:

- 5 (a) forming a molten body of silicon metal of two feedstocks of silicon, one feedstock containing a predetermined level of dopant;
- 10 (b) continuously drawing a single crystal ingot of doped silicon from said molten body of silicon, said ingot being characterized in that the concentration of dopant is uniform along the length of the ingot; while
- 15 (c) continuously feeding said feedstocks into said molten body of silicon to thereby maintain the concentration of dopant uniform in said body during the drawing of the single crystal therefrom.

2. The method comprising the steps of:

- 20 (a) feeding a first feedstock into a molten body of silicon metal, said first feedstock comprising ultrahigh purity semiconductor grade silicon;
- 25 (b) feeding a second feedstock into the aforesaid molten body of silicon, said second feedstock comprising ultrahigh purity semiconductor grade silicon to which a known amount of semiconductor dopant has been added; and
- 30 (c) while carrying out steps (a) and (b), drawing a single crystal of doped silicon from said molten body, steps (a), (b) and (c) being carried out on a substantially continuous basis.

3. The method of growing single crystal silicon comprising the steps of:

- 35 (a) forming a molten body of silicon of semiconductor grade;
- (b) introducing into said molten body a first silicon composition characterized in being spherical, low surface to volume ratio, nonagglomerated individual, monodisperse silicon particles

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having a diameter of about 1 mm;

(c) introducing into said molten body a second silicon composition characterized in being spherical, low surface to volume ratio, nonagglomerated individual, monodisperse doped silicon particles having a diameter of about 1 mm; and

(d) substantially continuously carrying out steps (b) and (c) while drawing from said molten body on a substantially continuous basis an ingot of doped silicon of semiconductor grade.

4. The process of Claim 3 wherein the doped silicon particles contain boron tribromide or phosphorous tribromide.

5. The process of Claim 3 wherein the silicon particles of step (b) and the doped silicon particles of step (c) have a mean diameter of about 1 mm and the particle feedstocks are substantially free of sintered particles, particles substantially over 2 mm in diameter and fine particles substantially under 1/2 mm in diameter.

6. The process of growing single crystal silicon ingots comprising the steps of:

(a) forming a molten body of silicon;

(b) drawing from said molten body a single crystal ingot of silicon; and

(c) introducing into said molten body at a controlled rate to make up silicon drawn therefrom individual silicon particles characterized as spherical, low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon having a diameter of from about 1/2 to about 2mm.

7. The continuous process of growing single crystal silicon ingots comprising the steps of:

(a) forming a molten body of silicon;

(b) introducing substantially continuously into said molten body at a controlled rate, individual silicon particles characterized as spherical,

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low surface to volume ratio, nonagglomerated individual monodisperse particles of silicon having a substantially uniform diameter; and

5 (c) drawing a single crystal ingot of semiconductor grade silicon from said molten body substantially at the rate at which silicon is introduced by way of the aforesaid particles.

8. The process of Claim 6 comprising the further step of:

10 (d) introducing into said molten body at a controlled rate, individual silicon particles differing from the first said particles characterized as spherical, low surface to volume ratio, nonagglomerated individual monodisperse
15 particles of silicon containing dopant having a substantially uniform diameter.

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US84/01980

I. CLASSIFICATION OF SUBJECT MATTER (If several classification symbols apply, indicate all) ¹ According to International Patent Classification (IPC) or to both National Classification and IPC <div style="text-align: center; font-weight: bold;">INT. CL. 3C 30B 15/04</div> <div style="text-align: center; font-weight: bold;">U.S. CL. 156/608; 156/617SP; 156/605</div>																	
II. FIELDS SEARCHED <div style="text-align: center; font-weight: bold;">Minimum Documentation Searched ⁴</div> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 25%;">Classification System</th> <th style="width: 75%;">Classification Symbols</th> </tr> <tr> <td style="text-align: center; vertical-align: middle; font-weight: bold;">U.S.</td> <td style="text-align: center; vertical-align: middle; font-weight: bold;">156/608, 617SP, 605, DIG 64</td> </tr> </table> <div style="text-align: center; font-weight: bold; margin-top: 10px;">Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁵</div>			Classification System	Classification Symbols	U.S.	156/608, 617SP, 605, DIG 64											
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<div style="display: flex; justify-content: space-between;"> <div style="width: 48%;"> <p>¹⁵ * Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> </div> <div style="width: 48%;"> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"&" document member of the same patent family</p> </div> </div>																	
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